

OPEN NETWORKING // Enabling Collaborative Development & Innovation

# Are You Insured Against Your Noisy Neighbor?

Sunku Ranganath, Intel Corporation Sridhar K.N. Rao, Spirent Communications

Hosted By

> OPEN NETWORKING // Enabling Collaborative Development & Innovation

## Legal Disclaimer

© 2018 Intel Corporation. Intel, the Intel logo, Intel Inside, the Intel Inside logo, Intel Experience What's Inside, The Intel Experience What's Inside logo, and Xeon are trademarks of Intel Corporation in the U.S. and/or other countries. \*Other names and brands may be claimed as the property of others.

Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Learn more at intel.com, or from the OEM or retailer.

Intel processors of the same SKU may vary in frequency or power as a result of natural variability in the production process.

For more complete information about performance and benchmark results, visit www.intel.com/benchmarks.

The cost reduction scenarios described are intended to enable you to get a better understanding of how the purchase of a given Intel based product, combined with a number of situation-specific variables, might affect future costs and savings. Circumstances will vary and there may be unaccounted-for costs related to the use and deployment of a given product. Nothing in this document should be interpreted as either a promise of or contract for a given level of costs or cost reduction.

Intel does not control or audit third-party benchmark data or the web sites referenced in this document. You should visit the referenced web site and confirm whether referenced data are accurate.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice Revision #20110804.

No computer system can be absolutely secure.

Intel<sup>®</sup> Advanced Vector Extensions (Intel<sup>®</sup> AVX)\* provides higher throughput to certain processor operations. Due to varying processor power characteristics, utilizing AVX instructions may cause a) some parts to operate at less than the rated frequency and b) some parts with Intel<sup>®</sup> Turbo Boost Technology 2.0 to not achieve any or maximum turbo frequencies. Performance varies depending on hardware, software, and system configuration and you can learn more at http://www.intel.com/go/turbo.

Hosted By

THELINUX FOUNDATION **TLF**NETWORKING

Available on select Intel® processors. Requires an Intel® HT Technology-enabled system. Your performance varies depending on the specific hardware and software you use. Learn more by visiting http://www.intel.com/info/hyperthreading.

§ Configurations: The testing was done on Based on fourth-generation Intel Xeon E5-2699 v4 @2.20 GHz processor with 22 cores, 55 MB LLC and 62 GB memory 16 1G hugepages. The testing was conducted in OPNFV Pharos testbed on Pod 12 by VSPERF community engineers

Intel, the Intel logo, [List the Intel trademarks in your document] are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries.

\*Other names and brands may be claimed as the property of others.

© Intel Corporation



#### 

OPEN NETWORKING // Enabling Collaborative Development & Innovation

# Acknowledgements

- Joseph Gasparakis
- Dakshina Illangovan
- Lin Yang
- Edwin Verplanke
- Priya Autee

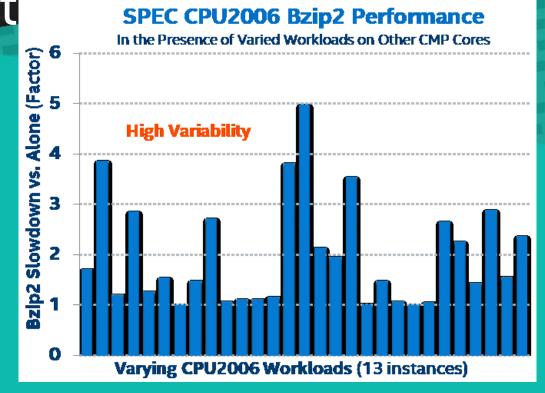
Hosted By



# Common Contentions in Cloud Deployment SPEC CPU2006 Bzip2 Performance

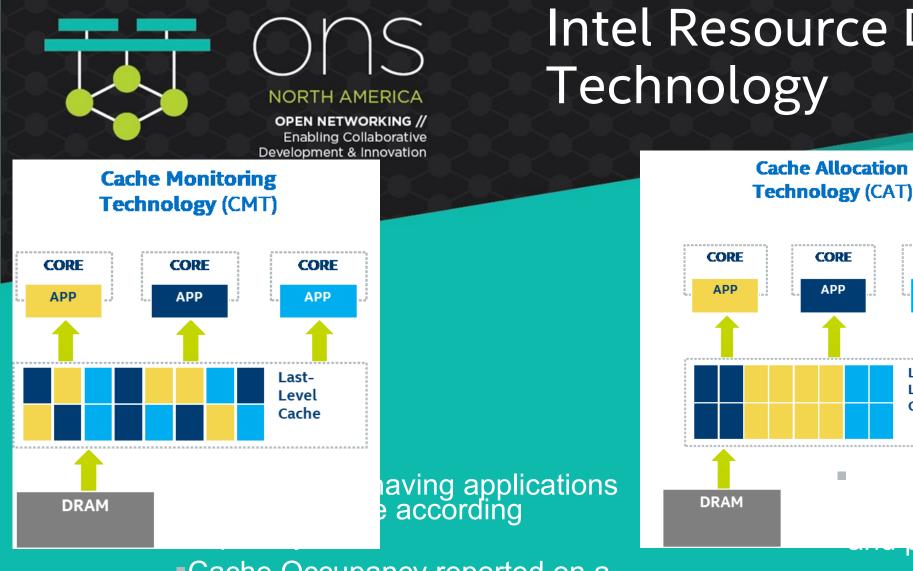
OPEN NETWORKING // Enabling Collaborative Development & Innovation

- Minimizing Total Cost of Ownership (TCO) often leads to oversubscription
- Quality of Service (QoS) requirements
- Lack of control of Last Level Cache (LLC) by orchestration layer



- Last-Level Cache Contention Can Lead to 51% Throughput Degradation1 in Comms Workloads
- Further: Last-Level Cache Contention Can Lead to Almost 5x Performance Variation

**NFV & RT workloads are Time Sensitive** 



Intel Resource Director

CORE

APP

Last-

Level

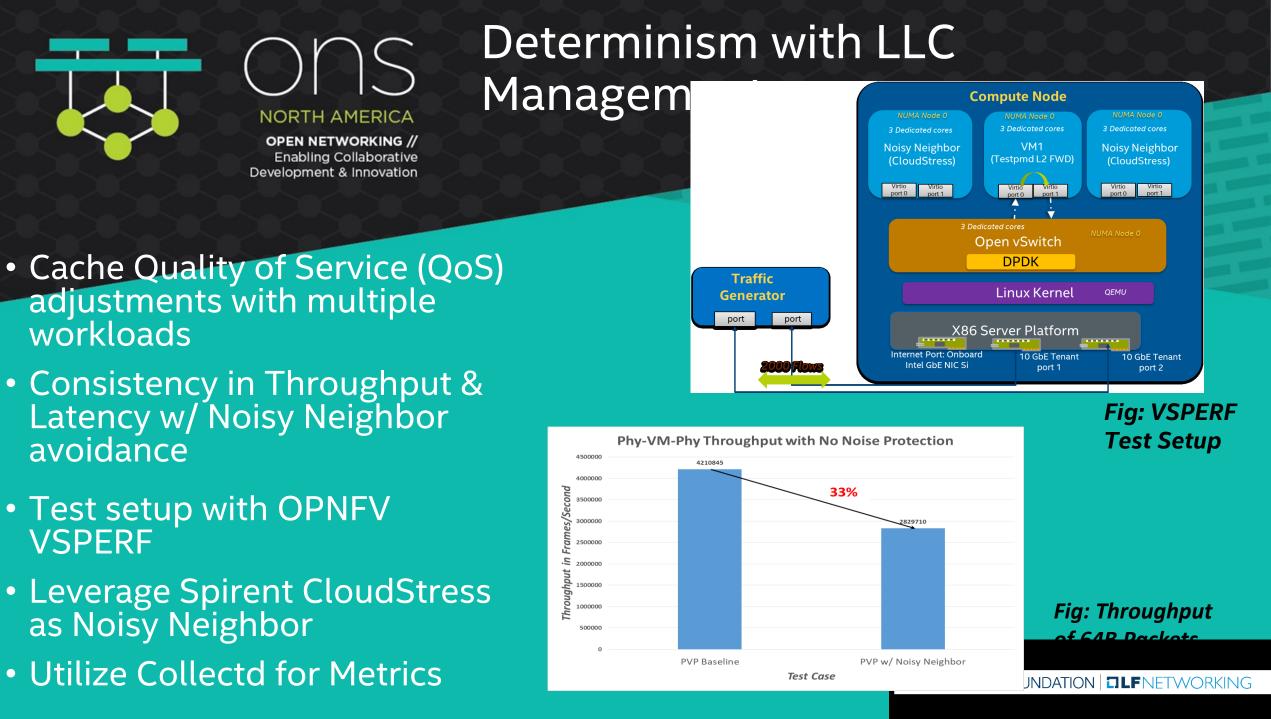
Cache

che partitioning nabling separation ion of apps or VMs

 Cache Occupancy reported on a per Resource Monitoring ID (RMID) basis—Advanced Telemétry

More Info: https://www.intel.com/content/www/us/en/architecture-and-technology/resource-director-technology.html

Misbehaving threads can be isolated to increase determinism Hosted By





NORTH AMERICA **OPEN NETWORKING // Enabling Collaborative** 

# **Development & Innovation**

# **Policy Driven LLC Allocation** - Resource Management Daemon

 Associate workload with dedicated LLC

• Construct LLC policy to protect workload from contention

**Resource Management Daemon** (RMD) - A Linux daemon that:

- Runs on individual hosts
- **REST API, accessible to orchestrator**
- Accepts & enforces policy
- **Platform Aware** •

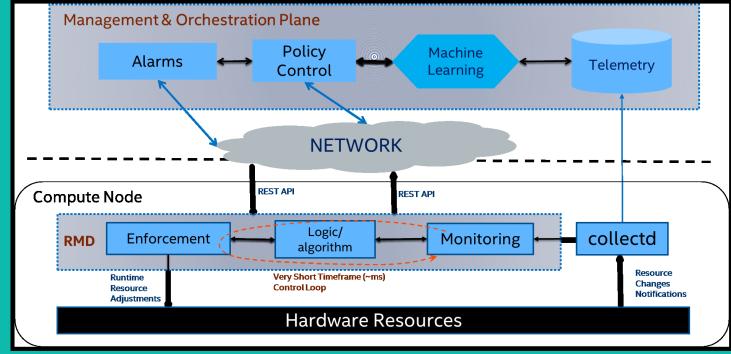


Fig: RMD interactions with Platform & MANO Layer

Open Source: https://github.com/intel/rmd

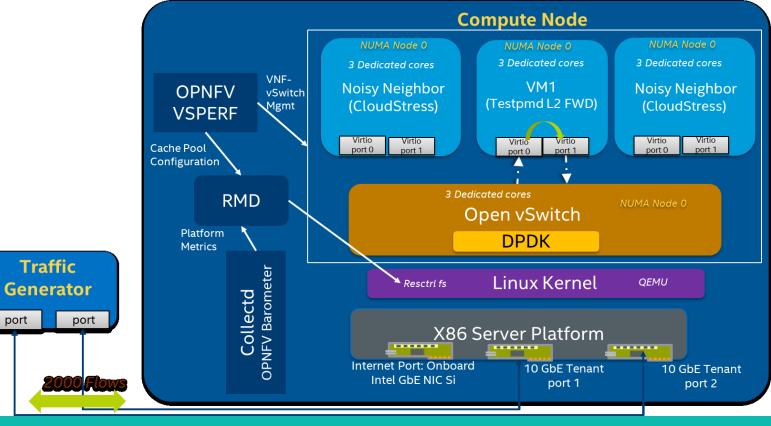


#### ONS NORTH AMERICA OPEN NETWORKING // Enabling Collaborative Development & Innovation

## Apply LLC policy at run time using RMD

- LLC for VM under test "Guaranteed" -2.5 MB
- LLC for CloudStress VMs "Best-effort" - 2.5 MB/VM

# • Re-run the performance tests



#### Fig: VSPERF Test setup with RMD & Collectd

Hosted B

**Run Time LLC Control via RMD Policy Mapping** 

# NORTH AMERICA NOISY Neighbor Protection & Takeaways

OPEN NETWORKING // Enabling Collaborative Development & Innovation

#### Guaranteed LLC policy helped preserve VM performance

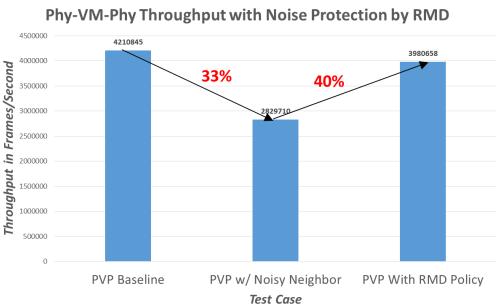
- Throughput improvement of ~40% with noise protection
- Integrating into OpenStack & Kubernetes

### **Takeaways**

- Noisy Neighbor affects are real and here to persist
- Intel RDT & RMD enables hardware infrastructure for LLC QoS control
- OPNFV VSPERF with RMD enables LLC QoS analysis for NFVi

#### **Optimal Cache Policy Ensures Deterministic Performance**

System configuration: Based on fourth-generation Intel Xeon E5-2699 v4 @2.20 GHz processor with 22 cores, 55 MB LLC and 62 GB memory 16 1G hugepages.



#### Fig: Throughput of 64B Packets

Hosted By